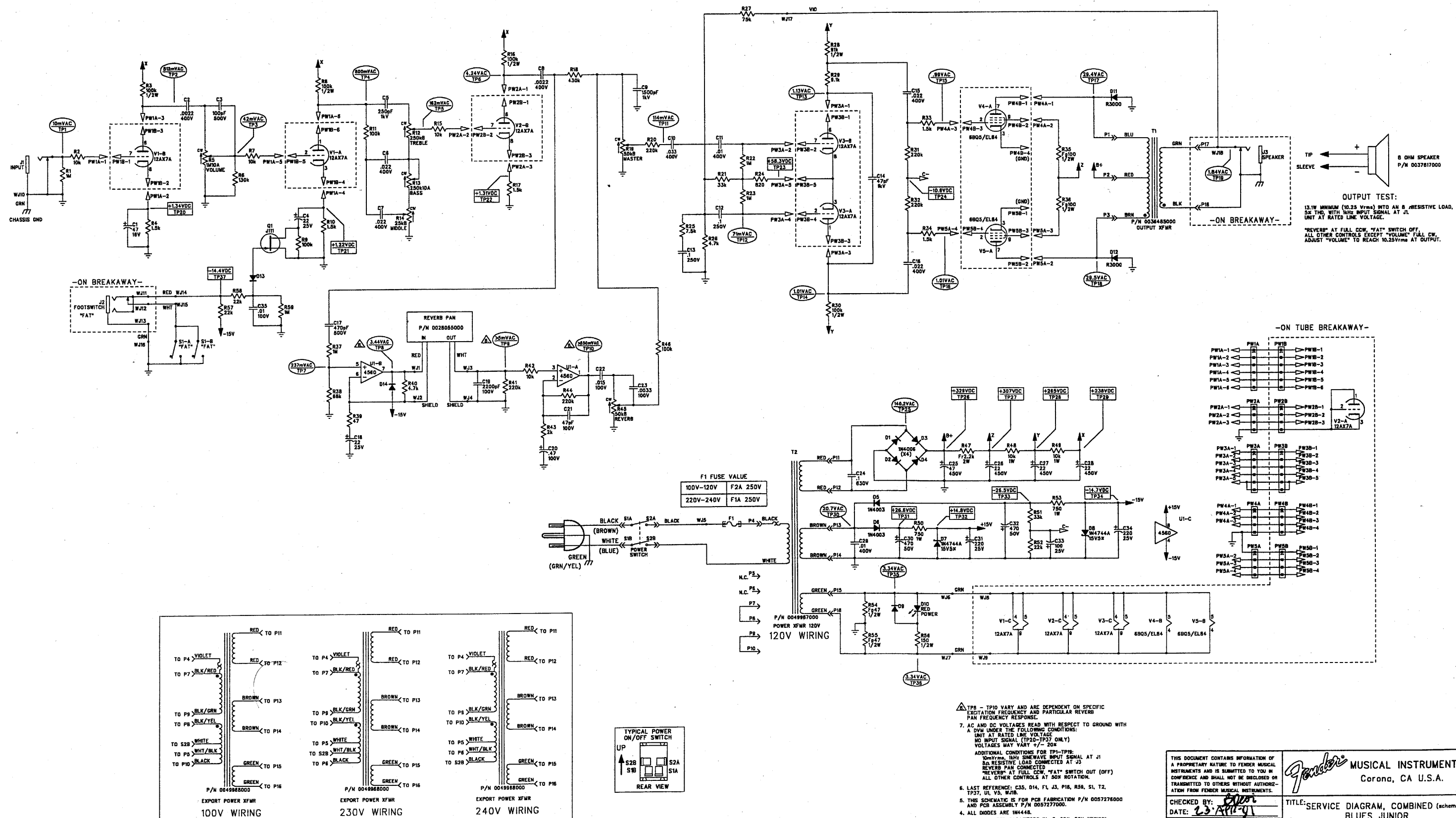


R E V I S I O N S			
REV.	DESCRIPTION	DATE	APPROVED
A	EC 2630	21-MAR-01	dbl



A. TP2 - TP10 VOLTAGE AND ARE DEPENDENT ON SPECIFIC
 EXCEPTED FREQUENCY AND PARTICULAR REVERSE
 PAN FREQUENCY RESPONSE.

- AC AND DC VOLTAGES READ WITH RESPECT TO GROUND WITH
 A DVM UNDER THE FOLLOWING CONDITIONS:
 INPUT RATED LINE VOLTAGE
 NO INPUT SIGNAL (TP20-TP37 ONLY)
 VOLTAGES MAY VARY $\pm 4\%$


ADDITIONAL CONDITIONS FOR TP1-TP9:
 1. WAVEFORM: 100% SINEWAVE INPUT SIGNAL AT J1
 2. RESISTIVE LOAD CONNECTED AT J3
 3. REVERSE PAN CONTROL
 4. "MENU" AT FULL CCG "FAT" SWITCH OUT (OFF)
 ALL OTHER CONTROLS AT 50X ROTATION.

- LAST REFERENCE: C35, D14, F13, P16, R56, S1, T2,
 TP37, U1, V5, W2B1.

THIS SCHEMATIC IS FOR PCB FABRICATION P/N 0057276000
 AND PCB ASSEMBLY P/N 0057277000.

- ALL DIODES ARE 1N4448s.
- ALL POLARIZED CAPACITORS IN μF , 20X; 50V MINIMUM.
- ALL UNPOLARIZED CAPACITORS IN μF , 10X OR BETTER; 50V
 (POWER SUPPLY BYPASS CAPACITORS ARE 20X).
- ALL RESISTORS IN OHMS, 5% ; 1/W.

NOTES: (UNLESS OTHERWISE NOTED)

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CHECKED BY: <u>ELDER</u> DATE: <u>23 APR 01</u>		TITLE: SERVICE DIAGRAM, COMBINED (schematic) BLUES JUNIOR	
APPROVED BY: <u>DES</u> DATE: <u>4/20/01</u>			
DRAWN: D. LEWIS ENGR: D. LEWIS		SIZE: D	DRAWING NUMBER: 0057275000
DATABASE FILE: Z48BS.SCH		REV. A	
		RELEASE DATE: 21-MAR-01	SHEET: 1 OF 2